

11-23-01

A

jc658 U.S. PTO



11/16/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: BAKER
Parent Serial No.: 09/452,341
Divisional Filed: November 16, 2001
Title: PHOTORESIST DISPENSE ARRANGEMENT BY COMPENSATION FOR SUBSTRATE REFLECTIVITY

Prior Examiner: Stevenson, A.
Prior Group Art Unit: 2812
Docket No.: PHA 51232A (VLSI.273DIV1)

jc996 U.S. PTO

09/990991



11/16/01

CERTIFICATE UNDER 37 CFR 1.10:
"Express Mail" mailing label number: EL 735813392 US
Date of Deposit: November 16, 2001

I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Assistant Commissioner for Patents, Washington, D.C. 20231

By: Kelly Walfugney
Kelly Walfugney

PATENT DIVISIONAL APPLICATION UNDER 37 C.F.R. § 1.53(b)

Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

Dear Sir:

Assistant Commissioner for Patents
BOX PATENT APPLICATION
Washington, DC 20231

Dear Sir:

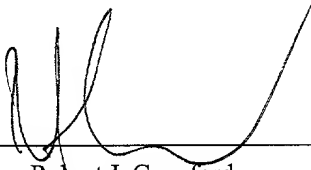
This is a request for filing a divisional application under 37 CFR § 1.53(b) of prior pending patent application Serial No. 09/452,341, filed on November 30, 1999, and entitled "PHOTORESIST DISPENSE METHOD BY COMPENSATION FOR SUBSTRATE REFLECTIVITY," for which the inventor is identified on the attached Declaration. We are transmitting herewith the following:

- ☒ A divisional application, with a new specification claiming priority to the prior application. The new application is as follows: 12 pages with 1 page of abstract, and 6 claims, 2 sheets of formal drawings having 4 Figures; and 3 pages of declaration from the prior application.
- ☒ Associate Power of Attorney, executed by the undersigned attorney of record.
- ☒ One return postcard.
- Payment of fees:**
 - ☒ Attached is a check in the amount of \$740.00 (VLSI.273PA) in payment of the filing fee.
- ☒ The Commissioner is hereby authorized to charge necessary, additional fees as set forth in 37 CFR §§ 1.16 to 1.18 which may be required by this paper or to credit any overpayment to Account No.50-0996 (VLSI.273PA).

6. ☒ The new specification has been amended to include before the first line the sentence:
"This is a divisional of Patent Application Serial No. 09/452,341, filed on November 30, 1999
(VLSI.273PA/PHA 51232), to which Applicant claims priority under 35 U.S.C. §120."
7. ☒ Priority of application Serial No. 09/452,341, filed on November 30, 1999 (VLSI.273PA) is hereby
claimed under 35 U.S.C. §120.
8. ☒ The prior application is assigned of record to VLSI Technology, Inc. (Koninklijke Philips
Electronics N.V.).
9. ☒ The Power of Attorney in the prior application is to: Crawford PLLC.
10. ☒ Address all future communications to the Attention of: **Philips Corporate Patent Counsel** at the
address below:

Direct all correspondence to the address below:

**Corporate Patent Counsel
Philips North America Corporation
580 White Plains Road
Tarrytown, NY 10591**

By: 
Name: Robert J. Crawford
Reg. No.: 32,122